AMENDMENTS TO THE CLAIMS

The following listing of claims replaces all prior versions:

1. (Cancelled)

2. (Previously Presented) The system of claim 8 wherein the current limiter prevents excess current from flowing from the SC to the battery.

3. (Previously Presented) The system of claim 8 wherein the SC prevents transients from the computer system from affecting the battery voltage.

4. (Currently Amended) The system of claim 3 wherein the SC has a capacitance of 20 farad and a resistance of ~~5 m~~ milliohms.

5. (Previously Presented) The system of claim 8 wherein the computer system comprises:

a power delivery subsection; and

a plurality of hardware components coupled to the power delivery subsection.

6. (Original) The system of claim 5 wherein the power delivery subsection comprises:

a system voltage regulator;

a chipset voltage regulator; and

a central processing unit (CPU) voltage regulator.

7. (Cancelled)

8. (Currently Amended) A system comprising:

a battery;

a super-capacitor (SC) coupled in parallel to the battery;

a computer system coupled to the battery and the SC; and

a current limiter, coupled to the battery, comprising:

a first transistor coupled to the battery;

a second transistor coupled to the first transistor to prevent excess current from flowing from the battery to the SC whenever the second transistor is deactivated; ~~and~~

a resistor coupled to the second transistor, the SC and the ~~computer.~~ computer system;

a first comparator with inputs coupled across the resistor and an output coupled to the gate of the second transistor; and

a second comparator with inputs coupled across the resistor and an output coupled to the gate of the first transistor.

9. (Original) The system of claim 8 wherein the first comparator deactivates the second transistor if the voltage across the resistor is greater than a first predetermined threshold.

10. (Original) The system of claim 9 wherein the second comparator deactivates the first transistor if the voltage across the resistor is greater than a second predetermined threshold.

11. (Currently Amended) A system comprising:

a battery;

a super-capacitor (SC) coupled in parallel to the battery;

a power delivery system coupled to the battery and the SC; and

a current limiter, coupled to the battery, comprising:

a first transistor coupled to the battery;

a second transistor coupled to the first transistor to prevent excess current from flowing from the battery to the SC whenever the second transistor is deactivated; ~~and~~

a resistor coupled to the second transistor, the SC and the ~~computer system.~~ power delivery system;

a first comparator with inputs coupled across the resistor and an output coupled to the gate of the second transistor; and

a second comparator with inputs coupled across the resistor and an output coupled to the gate of the first transistor.

12. (Original) The system of claim 11 wherein the current limiter prevents excess current from flowing from the SC to the battery.

13. (Previously Presented) The system of claim 11 wherein the SC prevents transients from the computer system from affecting the battery voltage.

14. (Original) The system of claim 11 wherein the power delivery system comprises:

a first voltage regulator; and

a second voltage regulator.

15. (Cancelled)

16 - 18. (Cancelled)